

Docket No. IRV1.PAU.30



RECEIVED
Patent Application
FEB -6 2002
2600 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Douglas M. Albert

Serial No.: 09/190,378

Filed: November 10, 1998

For: METHOD FOR THINNING
SEMICONDUCTOR WAFERS
WITH CIRCUITS AND WAFERS
MADE BY THE SAME

Examiner: Graybill, D

Group Art Unit: 2814

Irvine, California

November 14, 2001

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97(c) and 1.98, Applicant submits herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the above-identified patent application along with a copy of the references. Also enclosed is a check to cover the fee set forth in § 1.17(p).

Please charge any additional fees that may be due and credit any overpayment to our deposit account no. Account No. 01-1960. A duplicate copy of this paper is enclosed.



If the examiner believes that a telephone interview would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

RECEIVED

FEB -6 2002

2800 MAIL ROOM

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on November 14, 2001

By Angela Williams

Signature

November 14, 2001

Respectfully submitted,

Joseph C. Andras

Registration No. 33,469

MYERS DAWES & ANDRAS LLP

19900 MacArthur Boulevard, Suite 1150

Irvine, CA 92612

Tel: (949) 223-9600